



# Characterization of timing and spacial resolution of novel TI-LGAD structures before and after irradiation

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## ABSTRACT

The characterization of spacial and timing resolution of the novel Trench Isolated LGAD (TI-LGAD) technology is presented. This technology has been developed at FBK with the goal of achieving 4D pixels, where an accurate position resolution is combined in a single device with the precise timing determination for Minimum Ionizing Particles (MIPs). In the TI-LGAD technology, the pixelated LGAD pads are separated by physical trenches etched in the silicon. This technology can reduce the interpixel dead area, mitigating the fill factor problem. The TI-RD50 production studied in this work is the first one of pixelated TI-LGADs. The characterization was performed using a scanning TCT setup with an infrared laser and a <sup>90</sup>Sr source setup.

## 1. Introduction

The TI-LGAD technology aims to produce a silicon detector for Minimum Ionizing Particles (MIPs) with spacial resolution in the order of  $\lesssim 30 \mu\text{m}$ , temporal resolution of  $\lesssim 30 \text{ ps}$  and withstanding high levels of radiation of the order of  $\gtrsim 10^{15} \text{ n}_{\text{eq}} \text{ cm}^{-2}$  [1–3]. In this technology a two-dimensional array of small pixels is implemented in the surface of a silicon die. Each pixel is an LGAD, i.e. a silicon detector with a gain implant providing a multiplication in the order of  $\sim 5\text{--}20$ . The isolation between neighboring pixels is provided by physical trenches etched in the silicon [4].

### 1.1. The TI-RD50 production

The TI-RD50 production, carried out by FBK within the RD50 Collaboration<sup>1</sup> framework during 2020, implements several different designs with the objective of studying them and identifying the optimal ones. Fig. 1 illustrates each of the different design options. The first one is the *contact type* which determines how the contact between the external metalization and the n+ implant is realized, either in a “dot” or “ring” shape. The *number of trenches* separating each pixel can be either one or two. The *trench depth* can take 3 values, “D1” (shallow), “D2” (intermediate) and “D3” (deep). Finally, the *pixel border* is a measure of the distance between the trench and the gain implant and that can have 4 values “V1” (smallest, most aggressive), “V2”, “V3” and “V4” (largest, most conservative). Devices were produced for all possible

combinations of these variables. In this work, however, only devices with pixel border “V2” and “V3” were tested due to availability at the testing facility. All the devices studied in this work differ only by these variables, sharing all other design choices, such as the doses of each layer and the active thickness of  $45 \mu\text{m}$ .

### 1.2. Irradiation campaign

As mentioned, the TI-LGAD devices are aimed to operate in high radiation environments. One possible application is a replacement of pixel disks in the CMS experiment during the Phase-3 upgrades. Here, irradiation levels up to fluences of  $30 \times 10^{14} \text{ n}_{\text{eq}} \text{ cm}^{-2}$  are foreseen. Thus, we irradiated devices with reactor neutrons at JSI (Ljubljana) to three different fluences of 15, 25 and  $35 \times 10^{14} \text{ n}_{\text{eq}} \text{ cm}^{-2}$ . It has to be noted that the gain layer of most devices from the TI-RD50 production, and in particular those studied in this work, has not been optimized to withstand high fluences. In any case, the novelty here is the introduction of the trenches as the isolation mechanism between neighboring pixels, and our study focuses on this. Radiation hard gain layer designs and carbon coimplantation should be compatible with the trench isolated pixels in future productions.

## 2. Setup and measurements

The measurements were performed at the laboratory for silicon devices of the CMS group at UZH. A *scanning Transient Current Technique*

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<sup>1</sup> <https://rd50.web.cern.ch/>

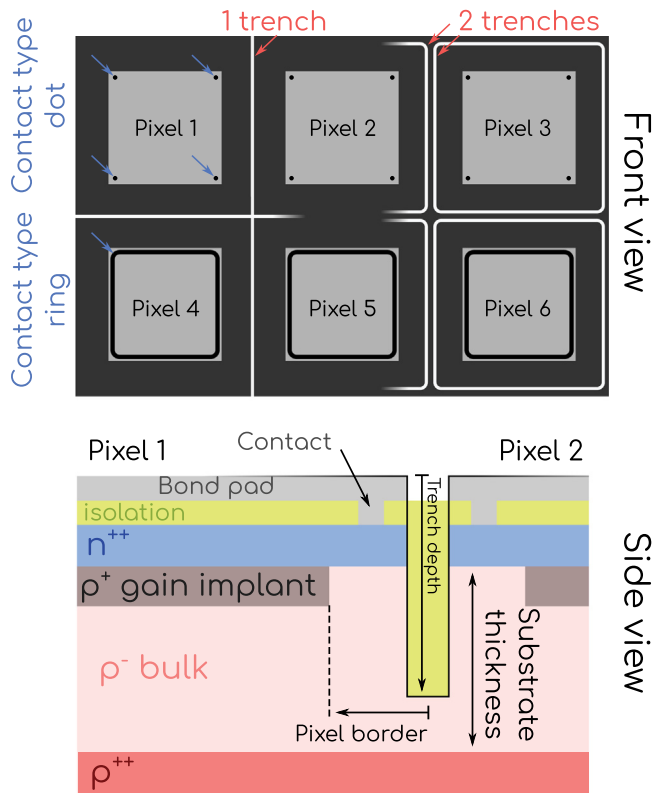


Fig. 1. Schematic representation of the different design patterns in the TI-RD50 production. In the front view the difference between *contact type dot* and *contact type ring* and 1 or 2 trenches is shown. In the side view the *trench depth* and *pixel border* variables are illustrated.

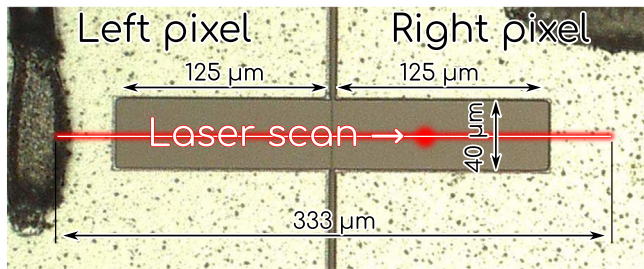


Fig. 2. Microscope picture of one device showing the area between two pixels and the opening in the metalization through which the laser was shone into the silicon. The thin dark line that can be seen vertically dividing each pixel is one trench.

(TCT) setup was used. This setup has an infrared laser (1064 nm) with a Gaussian profile with  $\sigma \approx 9 \mu\text{m}$ . The laser can be focused in arbitrary positions on the sample with a precision better than  $1 \mu\text{m}$  thus allowing to study the properties of the Device Under Test (DUT) as a function of position. In addition, the laser pulses are split in two paths, one containing 20 extra meters of optic fiber, before being sent to the DUT. This produces two almost identical pulses of light with a fixed temporal separation of 100 ns and allows to study the time resolution of the DUT.

The samples were installed in a custom made passive readout board. This board had incorporated a temperature and humidity sensor very close to the DUT, which was used to stabilize the temperature at  $-20^\circ\text{C}$  during the measurement of irradiated devices. The samples had either 4 or 16 pixels, but only two of them were used while the others were connected to ground. In particular, pixels with a special opening in the metalization were measured, which allow the light from the laser to reach the silicon, as shown in Fig. 2.

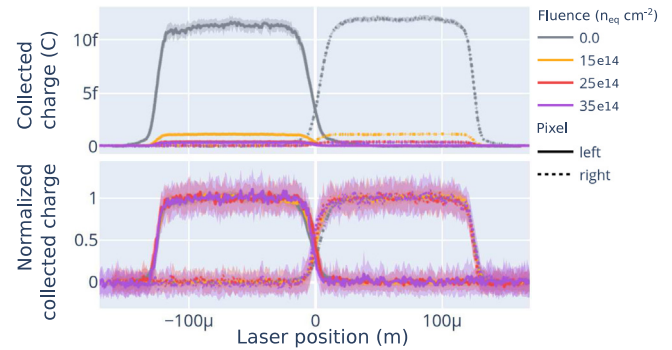


Fig. 3. Collected charge and normalized collected charge as a function of position for four identical devices (contact “dot”, 1 trench, depth “D1”, border “V2”) irradiated to different fluences. Each trace was normalized individually between 0 and 1.

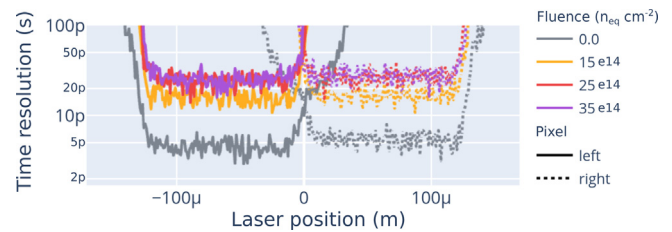


Fig. 4. Time resolution as measured with the TCT as a function of position for the same four devices shown in Fig. 3.

Each pixel was connected to a broadband fast amplifier (Cividec C2HV) and then into the oscilloscope, which was either a LeCroy WaveRunner 640Zi or 9254M. The samples were biased using a Keithley 2470 high voltage power supply, which was also used to trace the IV curves.

An example of the charge collected as a function of position is shown in Fig. 3. The inter-pixel distance was defined as the distance between the two positions where 50 % of the maximum charge is collected in the left and right pixels, which is a criterion that was also used in the past in other works [5,6].

The time resolution was calculated at each position by shining the laser many times and then applying the constant fraction discriminator method [6] between each of the two pulses produced after the laser splitting system described before.

The laser intensity for all the measurements was configured such that the mean value of the collected charge is close to the most probable value of the distribution obtained with beta particles from a radioactive  $^{90}\text{Sr}$  source. This calibration was done prior to the measurements.

### 3. Results

For each value of *contact type*, *number of trenches*, *trench depth* and *pixel border* a device was measured, and the same method and data analysis were applied to all the samples. In the following subsections some of the most relevant measured parameters will be presented.

#### 3.1. Pixel isolation provided by the trenches

The results in Fig. 3 show an excellent pixel isolation for all the fluences studied as no cross-talk could be observed in any case. This demonstrates that the isolation provided by the trenches is very good and withstands without issues fluences of at least  $35 \times 10^{14} \text{ n}_{\text{eq}} \text{ cm}^{-2}$ .

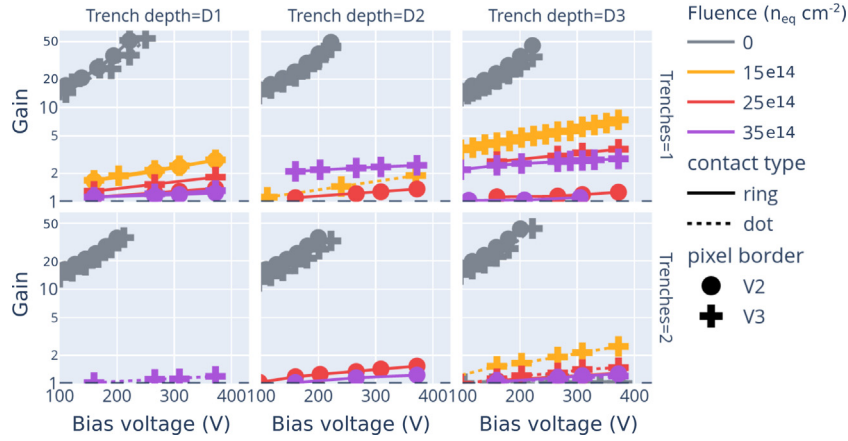


Fig. 5. Measured gain vs bias voltage for each of the different design patterns and fluences studied.

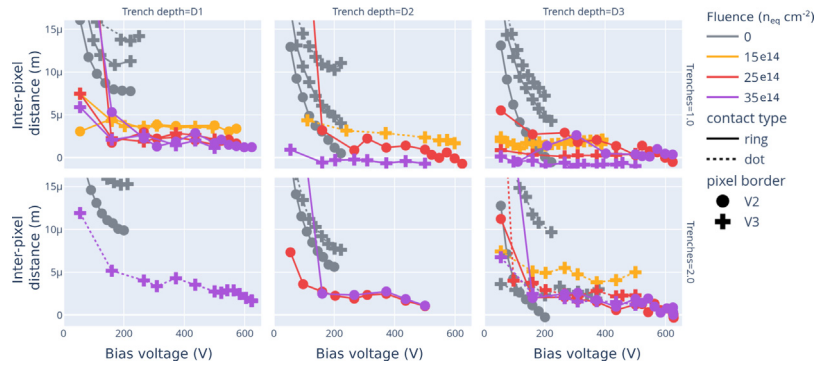


Fig. 6. Measured inter-pixel distance as a function of the bias voltage for each of the different design patterns.

### 3.2. Gain

For each of the devices studied the gain was computed as the ratio between the collected charge by the DUT and the collected charge by a non irradiated PIN diode<sup>2</sup> of the TI-RD50 production. The results are shown in Fig. 5 as a function of the bias voltage. We see that before irradiation the gain reaches values between 30 and 50 at the operating voltage of 200 V while after irradiation it is severely degraded to values between 3 and 8 for the lowest fluence and between 1.2 and 3 for the highest fluence. It has to be noted that for the irradiated devices the gain could not be measured at higher voltages than 400 V as to the reference PIN diode went into breakdown. At 600 V a slightly higher gain was observed as the collected charge was larger, anyhow its precise value could not be determined due to the breakdown of the reference PIN diode. We do not observe any systematic dependence of the gain on each of the design patterns studied.

### 3.3. Inter-pixel distance

The inter-pixel distance (IPD) is one of the most relevant properties analyzed in this study. It was measured for each design pattern at each different irradiation fluence for different values of the bias voltage. The results are shown in Fig. 6. It can be seen that before irradiation this quantity has an important dependence with the bias voltage as well as with the different design variables. We note that the designs with Pixel border V2 achieve lower IPD values than V3, as well as deeper trenches with respect to shallow ones, single trenches with respect to double trenches, and contact type ring with respect to dot. After irradiation all these dependencies on the design features are much less relevant.

We still observe a small improvement with increasing bias voltage. In a previous study on non irradiated TI-LGADs the IPD was also seen to improve by increasing the bias voltage [7].

### 3.4. Time resolution

The time resolution is another important quantity that was measured for the TI-RD50 production. The results are shown in Fig. 7. As previously discussed the time resolution was determined in the TCT setup using a constant fraction discriminator method. The first observation is that prior to irradiation all devices show roughly the same time resolution, achieving a value of about 5 ps at 200 V. The time resolution shows no dependence with the different design patterns studied. After irradiation we see that the time resolution is degraded, however it is still good. All the devices measured up to 600 V show time a resolution on the order of 20 ps.

As can be observed in Fig. 4 the time resolution is practically uniform along the pixel up to very close to the inter-pixel area. This is an important characteristic as it makes the time resolution independent of the impact position.

## 4. Measurements with beta source

In order to investigate the contribution of the Landau term to the time resolution of the TI-LGADs [6], a subset of the irradiated devices was measured with a radioactive <sup>90</sup>Sr beta source setup. These measurements are essential to give a measurement of the timing resolution that is comparable with the expected performance of the pixelated LGAD devices in a real HEP detector environment. The same constant fraction discriminator analysis was performed with one of the pulses being that from the DUT while the other being that of a reference

<sup>2</sup> I.e. a device with no gain layer.

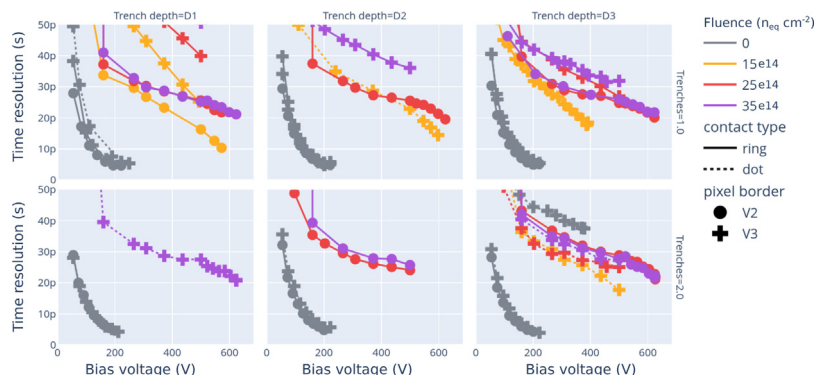


Fig. 7. Measured time resolution (in the TCT) as a function of the bias voltage for each of the different design patterns.

detector, which was a previously calibrated single pad LGAD device. The electrical connections for the DUT were kept completely the same as in the TCT setup. In this case time resolution between 50 and 65 ps was observed for devices irradiated at  $35 \times 10^{14} \text{ n}_{\text{eq}} \text{ cm}^{-2}$ .

### 5. Annealing effects

Three devices, all with the same design but with different values of fluence, were measured in the TCT after irradiation and then left to anneal for 7 days at room temperature. After this annealing period the TCT measurements were repeated and compared with those before annealing. The effects of the annealing were observed to behave systematically in the three detectors. Such effects demonstrated no improvement in their performance, with the exception of the bias current which was reduced approximately by a factor of 40 %. The IPD was unaffected by the annealing process while the gain was reduced by a factor between 10 and 20 % and the time resolution worsened by a factor between 15 to 20 %.

### 6. Conclusions

The first production of trench isolated LGAD pixelated devices, TI-RD50, was extensively characterized both before and after being irradiated with neutrons up to fluence values of  $35 \times 10^{14} \text{ n}_{\text{eq}} \text{ cm}^{-2}$ . The inter-pixel distance of many of the different designs in this production was studied using a scanning TCT setup. It was found that not all the designs behave the same and the that the best combination among those studied is one trench, deep, with pixel border V2 and contact type ring. It was observed that after irradiation the inter-pixel distance becomes less dependent on the bias voltage as well as on the different design patterns.

The pixel isolation provided by the trenches was observed to be very good both before and after irradiation and for all the different designs. The cross-talk levels were in all cases below the noise floor of the measurement setup.

The time resolution was studied. Values on the order of 5 ps were observed before irradiation while values on the order of 20 ps were observed after irradiation, both using infrared laser in the TCT setup and beta source. As opposed to the inter-pixel distance, the time resolution did not show a systematic dependence on the different design patterns. In order to investigate the expected timing performance with MIPs, for

a few irradiated samples the time resolution was also measured with a beta source and values on the order of 50–65 ps were observed.

From these results we conclude that the TI-LGAD technology is a promising candidate for the implementation of the 4D-pixels as they can provide good time and space resolution with reasonable fill factor while withstanding high radiation levels.

### Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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### References

- [1] Giovanni Paternoster, Latest developments on trench-isolated LGADs, in: Presented at the 35th RD50 Workshop, CERN, November 19, 2019, <https://indico.cern.ch/event/855994/contributions/3637012/>.
- [2] Nicolo Cartiglia, 4D-tracking, LGADs, and fast timing detectors, in: Presented at the VCI2022 - the 16th Vienna Conference on Instrumentation, Vienna, February 25, 2022, <https://indico.cern.ch/event/1044975/contributions/4566014/>.
- [3] G. Paternoster, G. Borghi, R. Arcidiacono, M. Boscardin, N. Cartiglia, M. Centis Vignali, G.F. Dalla Betta, et al., Novel strategies for fine-segmented low gain avalanche diodes, Nucl. Instrum. Methods Phys. Res. A 987 (January) (2021) 164840, <http://dx.doi.org/10.1016/j.nima.2020.164840>.
- [4] G. Paternoster, G. Borghi, M. Boscardin, N. Cartiglia, M. Ferrero, F. Fiorella, F. Siviero, A. Gola, P. Bellutti, Trench-isolated low gain avalanche diodes (TI-LGADs), IEEE Electron Device Lett. 41 (6) (2020) 884–887, <http://dx.doi.org/10.1109/LED.2020.2991351>.
- [5] Federico Siviero, Laboratory measurements of FBK trench-isolated LGADs in torino, in: Presented at the 35th RD50 Workshop, CERN, November 19, 2019, <https://indico.cern.ch/event/855994/contributions/3637009/>.
- [6] Marco Ferrero, Roberta Arcidiacono, Marco Mandurino, Valentina Sola, Nicolò Cartiglia, An Introduction To Ultra-Fast Silicon Detectors: Design, Tests, and Performances, CRC Press, Boca Raton, 2021, <http://dx.doi.org/10.1201/9781003131946>.
- [7] Ashish Bisht, Characterization of novel trench-isolated LGADs for 4D tracking, in: Presented at the Workshop on Pico-Second Timing Detectors for Physics, Zurich, September 9, 2021, <https://indico.cern.ch/event/861104/contributions/4514658/>.